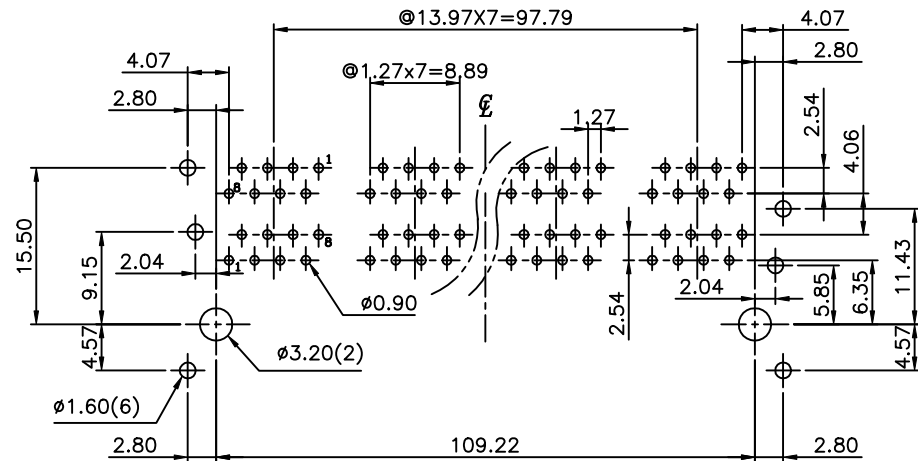
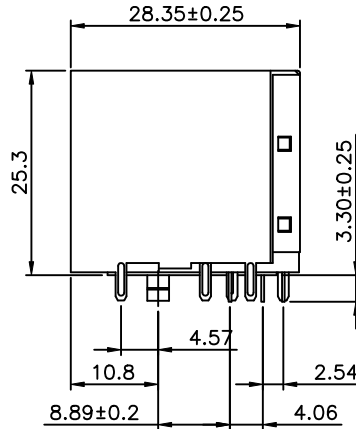
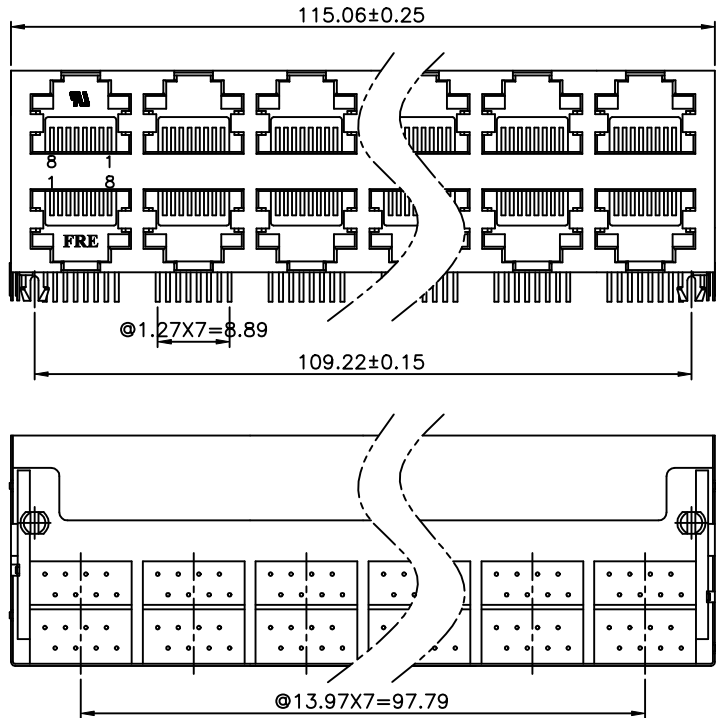


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

NOTES:

ELECTRICAL:

- 1. VOLTAGE RATING : 125 VAC RMS.
- 2. CURRENT RATING : 1.5 AMP.
- 3. CONTACT RESISTANCE : 50 MILLIOHMS MAX.
- 4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
- 5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

- 1. HOUSING MATERIAL : NYLON+30%.G.F UL94V-0.
- 2. CONTACT MATERIAL : PHOSPHOR BRONZE φ0.46mm.
- 3. PLATING : GOLD PLATING OVER NICKEL.
- 4. OPERATING LIFE : 750 CYCLES MIN.
- 5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
- 6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

- 1. STORAGE : -40°C TO +85°C.
- 2. OPERATION: 0°C TO 70°C.
- 3. WAVE SOLDERING TEMPERATURE: 255°C~265°C(5~10 SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

ROHS PART NUMBER: E5908-1XG13X-L

Shield: Gold Thickness:
1- Tin 1- 3u" 2- 6u" 3-15u"
7- Nickel 4-30u" 5-50u"

DETACHED LISTS	MM (INCH)	DFTO JAMY	DATE 9/13/05
	TOLERANCES EXCEPT AS NOTED	CHKD SAMMY	DATE 2009/09/14
		MFO	DATE
	MM	APPVL LUSHENG	DATE 2005/09/14
	.0 ±0.10		
	.00 ±0.05		
	.000 ±0.005		
	ANGLES ± 0.01°	QT'Y :	
THIRD ANGLE PROJECTION	FINISH :		
	SCALE : 2:1		

FULL RISE ELECTRONIC CO., LTD			
TITLE STACKED GANGED JACK, 2X8			
DRAWING NO. GE593B04-LA		SIZE	REV
/PART NO. SEE NOTE		A3	0
DO NOT SCALE DRAWING		SHEET X	OF Y