

QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH DOUBLE-WIDTH BUS SWITCH

IDTQS32X245

FEATURES:

- · Enhanced N channel FET with no inherent diode to Vcc
- 5Ω bidirectional switches connect inputs to outputs
- · Dual '245 function
- · Zero propagation delay, zero ground bounce
- · Undershoot clamp diodes on all switch and control inputs
- · TTL-compatible control inputs
- · Available in 40-pin QVSOP package

APPLICATIONS:

- · Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- · Bus switching and isolation
- Power conservation
- Clock gating
- · Logic replacement

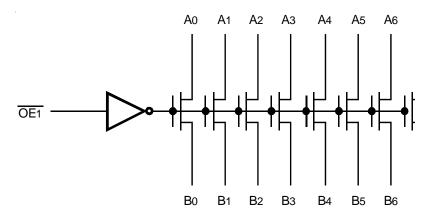
DESCRIPTION:

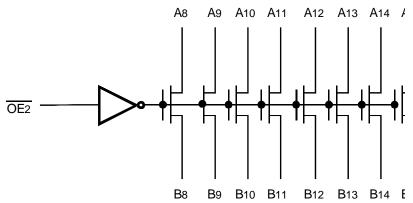
The QS32X245 provides a set of 16 high-speed CMOS TTL-compatible bus switches in a flow-through pinout. The low ON resistance of the QS32X245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The Output Enable (\overline{OEn}) signals turn the switches on similar to the \overline{OEn} signal of the 74'245.

QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

The QS32X245 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



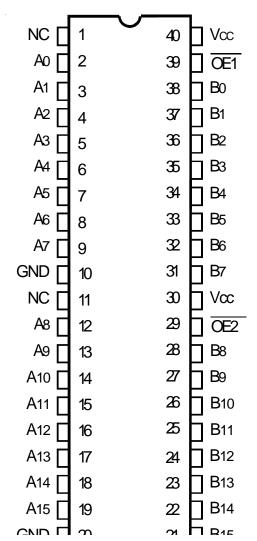


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INDUSTRIAL TEMPERATURE RANGE

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PIN CONFIGURATION



QVSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	-0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	-0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage Vเง	-0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤ 20ns)	-3	V
lout	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.92	W
Tstg	Storage Temperature	-65 to +150	°C

NOTE:

- 1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc terminals.
- 3. All terminals except Vcc.

CAPACITANCE

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$

Pins	Тур.	Max. ⁽¹⁾	Unit
Control Pins	3	5	pF
Quickswitch Channels (Switch OFF)	5	7	pF

NOTE:

1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

Pin Names	I/O	Description
OE1, OE2	I	Bus Enable
An	I/O	Bus A
Bn	I/O	Bus B

FUNCTION TABLE(1)

ŌĒ1	ŌĒ2	A0 - A7	A 8 - A 15	Function	
Н	Н	Z	Z	Disconnect	
L	Н	Bo - B7	Z	Connect	
Н	L	Z	B8 - B15	Connect	
L	L	B0 - B7	B8 - B15	Connect	

NOTE

- 1. H = HIGH Voltage Level
 - L = LOW Voltage Level
 - Z = High-Impedance

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

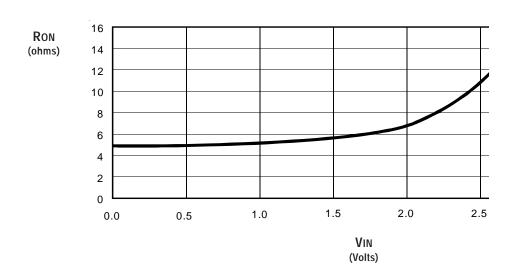
Industrial: TA = -40°C to +85°C, $VCC = 5.0V \pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
VIH	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	_	-	V
VIL	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
lin	Input LeakageCurrent (Control Inputs)	$0V \le VIN \le VCC$	_	±0.01	±1	μA
loz	Off-State Output Current (Hi-Z)	0V ≤ Vouт ≤ Vcc, Switches OFF	_	±0.01	±1	μA
Ron	Switch ON Resistance	VCC = Min., VIN = 0V, ION = 30mA	_	5	7	Ω
		VCC = Min., VIN = 2.4V, ION = 15mA	_	10	15	
VP	Pass Voltage ⁽²⁾	$V_{\text{IN}} = V_{\text{CC}} = 5V$, $I_{\text{OUT}} = -5\mu A$	3.7	4	4.2	V

NOTES:

- 1. Typical values are at Vcc = 5.0V, TA = 25°C.
- 2. Pass Voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
Icco	Quiescent Power Supply Current	Vcc = Max., Vin = GND or Vcc, f = 0	6	μΑ
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., Vin = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., A and B pins open	0.25	mA/MHz
		Control Inputs Toggling at 50% Duty Cycle		

NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TLL driven input ($V_{IN} = 3.4V$, control inputs only). A and B pins do not contribute to Δlcc .
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{CC} = 5.0V \pm 5\%$

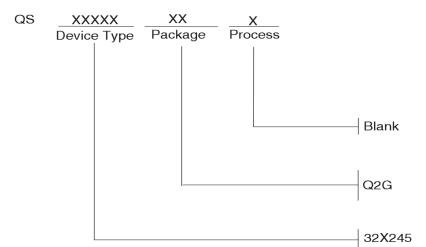
CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
tplh	Data Propagation Delay ^(2,3)	_	_	0.25	ns
tphl.	An to/from Bn				
tpzl	Switch Turn-on Delay	0.5	_	5.6	ns
tpzh	OEn to An/Bn				
tplz	Switch Turn-off Delay (2)	0.5	_	4.5	ns
tphz	OEn to An/Bn				

NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



Datasheet Document History

01/08/09 Pg. 5 Updated the ordering information by removing the "IDT" notation and non RoHS part.



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