SN54153, SN54LS153, SN54S153 SN74153, SN74LS153, SN74S153 **DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS** SDLS055A - DECEMBER 1972 - REVISED MAY 2007

- Permits Multiplexing from N lines to 1 line
- Performs Parallel-to-Serial Conversion •
- Strobe (Enable) Line Provided for Cascading (N lines to n lines)
- High-Fan-Out, Low-Impedance, Totem-Pole Outputs
- Fully Compatible with most TTL Circuits

ТҮРЕ		YPICAL AVERA		TYPICAL POWER
	FROM DATA	FROM STROBE	FROM SELECT	DISSIPATION
153	14 ns	17 ns	22 ns	180 mW
LS153	14 ns	19 ns	22 ns	31 mW
′S153	6 ns	9.5 ns	12 ns	225 mW

description

Each of these monolithic, data selectors/multiplexers contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR gates. Separate strobe inputs are provided for each of the two four-line sections.

FUNCTION TABLE

ł	ECT UTS		DATA	INPUT:	S	STROBE	OUTPUT
B	A	CO	C1	C2	C3	Ğ	Y
x	×	x	X	х	x	н	L
L	L	L	х	х	x	Ł	L
L L	L	н	х	x	х	L	н
L	Н	X	L	×	×	L	L
L	н	×	н	x	×	L	н
н	L	x	х	L	x	L	L
н	L	x	х	н	×	L.	н
н	н	×	х	х	L L	L	L
н	Н	Х	X	Х	н	L	н

Select inputs A and B are common to both sections. H = high level, L = low level, X = irrelevant

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (See Note 1) .	
Input voltage: '153, 'S153	5.5 V
′LS153	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
Operating free-air temperature range:	SN54' 55°C to 125°C
	SN74' 0°C to 70°C
Storage temperature range	

NOTE 1: Voltage values are with respect to network ground terminal.

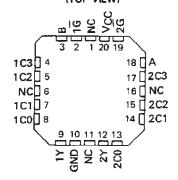
PRODUCTION DATA documents cantain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN54153, SN54LS153, SN54S153 J OR W PACKAGE ⁽¹⁾
SN74153 N PACKAGE
SN74LS153, SN74S153, D OR N PACKAGE

	тс	P VIEW)
1G[1	
вС	2	15 🗌 2 G
1C3 [3	14 🗖 A
1C2[4	13 🛛 2C3
101	5	12 🗌 2C2
1 CO 🗌	6	11 🛛 2C1
1Y 🗌	7	10 🛛 2 CO
GND	8	9 🗌 2Y

SN54LS153, SN54S153 ... FK PACKAGE (1) (TOP VIEW)

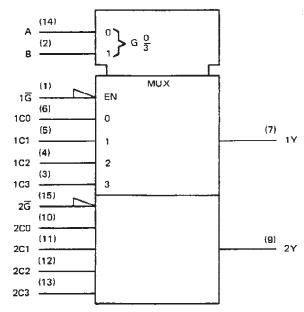


NC - No internal connection

(1) SN54S153, SN74153, and SN74S153 are obsolete.

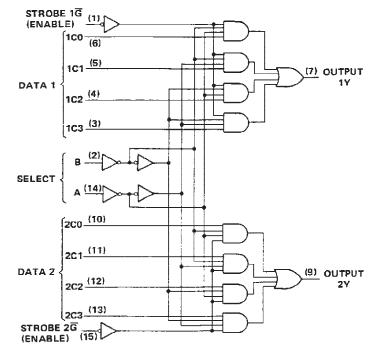
SN54153, SN54LS153, SN54S153 SN74153, SN74LS153, SN74S153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

logic symbol[†]



 † This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

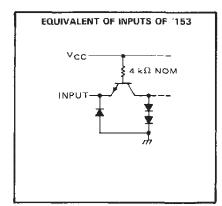
logic diagrams (positive logic)

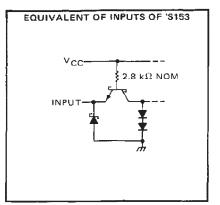


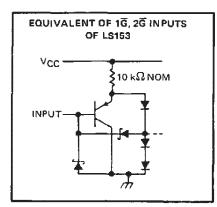
Pin numbers shown are for D, J, N, and W packages.

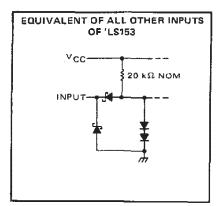


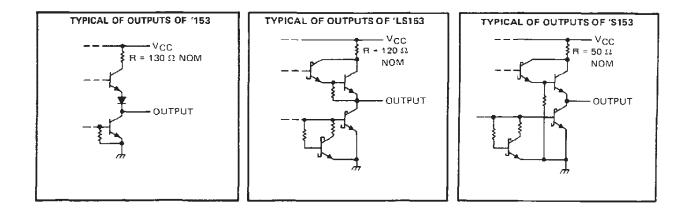
schematics of inputs and outputs













SN54153, SN74153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

recommended operating conditions

		SN54153					UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
Supply voltage, V _{CC}	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH			-800			-800	μA
Low-level output current, IOL			16			16	mA
Operating free-air temperature, TA	55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				SN5415	3		3		
	PARAMETER	TEST CONDITIONS [†]	MIN	TYP [‡]	MAX	MIN	түр‡	MAX	UNIT
ViH	High-level input voltage		2			2			V
VIL	Low-level input voltage				0.8			0.8	V
VIK	Input clamp voltage	$V_{CC} = MIN, I_1 = -12 mA$			-1,5			-1.5	V
VOH	High-level output voltage	$V_{CC} = MIN, V_{1H} = 2V,$ $V_{1L} = 0.8V, I_{OH} = -800 \mu A$	2.4	3.4		2.4	3.4		V
VoL	Low-level output voltage	V _{CC} = MIN, V _{IH} = 2 V, V _{IL} = 0.8 V, I _{OL} = 16 mA	-	0.2	0.4		0.2	0.4	v
ty	Input current at maximum input voltage	V _{CC} = MAX, V ₁ = 5.5 V			1			1	mΑ
hн	High-level input current	V _{CC} = MAX, V _I = 2.4 V			40			40	μA
41	Low-level input current	V _{CC} = MAX, V _I = 0.4 V			-1.6	1		-1.6	mA
los	Short-circuit output current §	V _{CC} = MAX	-20		55	18		-57	mA
ICCL	Supply current, output fow	V _{CC} = MAX, See Note 2		36	52		36	60	mA

[†]For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡]All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

SNot more than one output should be shorted at a time.

NOTE 2: ICCL is measured with the outputs open and all inputs grounded.

switching characteristics, VCC = 5 V, TA = 25° C

PARAMETER¶	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	түр	MAX	UNIT
tPLH	Data	Y			12	18	ns
tpHL	Data	Y	-1		15	23	ns
^t PLH	Select	Y	C _L = 30 pF, R _L = 400 Ω,		22	34	ns
^t ₽HL	Select	Y	See Note 3		22	34	п\$
^t PLH	Strobe G	Y	1		19	30	กร
tPHL	Strobe G	Y	7		15	23	กร

 f_{tPLH} = propagation delay time, low-to-high-level output

tpHL = propagation delay time, high-to-low-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



SN54LS153, SN74LS153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

recommended operating conditions

		SN5	SN54LS153			SN74LS153			
		MIN	NOM	MAX	MIN	NOM	МАХ	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5,25	V.	
⊻ін	High-level input voltage	2			2			v	
VIL	Low-level input voltage			0.7			0.8	V	
юн	High-level output current			- 0.4			- 0.4	mА	
IOL	Low-level output current			4			8	mΑ	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS 1		s	N54LS1	53	s			
FANAMETER	TEST CONDITIONS I		MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
Vik	V _{CC} = MIN, I ₁ = - 18 mA				- 1.5			- 1.5	V
Voн	$V_{CC} = MIN$, $V_{IH} = 2V$, $V_{IL} = MA$ $I_{OH} = -0.4 \text{ mA}$	x	2.5	3.4		2.7	3.4		v
Va	$V_{CC} = MIN, V_{IH} = 2 V,$	IOL=4mA		0.25	0.4		0.25	0.4	v
VOI I	VIL = MAX,	IOL = 8 mA					0.35	0.5	1 Y
14	VCC=MAX, VI=7V				0.1			0.1	mA
Чн	V _{CC} = MAX, V ₁ = 2.7 V	· · ·	1		20			20	μA
1G, 2G	Vcc = MAX, VI = 0.4 V				- 0.2			-0.2	
All other	V _{CC} = MAX, VI = 0.4 V				- 0.4			- 0.4	mA
los§	VCC = MAX	•	- 20		- 100	- 20		- 100	mA
ICCL	V _{CC} = MAX, See Note 2	_		6.2	10		6.2	10	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \ddagger All typical values are at V $_{CC}$ = 5 V, τ_{A} = 25 $^{\circ}$ C.

Not more than one output should be shorted at a time.

NOTE 2: ICCL is measured with the outputs open and all inputs grounded.

то FROM PARAMETER TEST CONDITIONS MIN ТҮР MAX UNIT (INPUT) (OUTPUT) 10 15 Data Y ns **TPLH** 17 26 TPHL Data Y ПS CL = 15 pF, 19 Select Y 29 ПS ^tPLH $R_L = 2 k\Omega$, 25 Select Y 38 ns ^tPHL See Note 3 V 16 24 ns TPLH Strope G 21 32 ^tPHL Y ns Strobe G

switching characteristics, VCC = 5 V, TA = 25°C

 f_{tpLH} = propagation delay time, low-to-high-level output

tpHL = propagation delay time, high-to-low-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



SN54S153, SN74S153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

recommended operating conditions

	S	SN54S153			SN74S153		
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, VCC	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH			-1			-1	mΑ
Low-level output current, IOL			20			20	mA
Operating free-air temperature, T _A	-55		125	0		70	´C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CONDITIONS [†]	MIN	TYP ‡	MAX	UNIT
⊻ін	High-level input voltage		2			V
VIL	Low-level input voltage				0.8	V
VIK	Input clamp voltage	V _{CC} = MIN, I ₁ = -18 mA			-1.2	V
V	the level output voltage	V _{CC} = MIN, V _{IH} = 2 V, Series 54	S 2.5	3.4		v
∨он	High-level output voltage	VIL = 0.8 V, IOH = -1 mA Series 74	\$ 2.7	3.4		V V
.,		$V_{CC} = MIN, V_{IH} = 2V,$		-	0.5	v
VOL	Low-level output voltage	V _{IL} = 0.8 V, I _{OL} = 20 mA			0.5	v
4	Input current at maximum input voltage	V _{CC} = MAX, V _I = 5.5 V			1	mА
ίн	High-level input current	V _{CC} = MAX, V ₁ = 2.7 V			50	μA
4L	Low-level input current	V _{CC} = MAX, V ₁ = 0.5 V		······	-2	mA
los	Short-circuit output current §	V _{CC} = MAX	-40		-100	mΑ
CCL	Supply current, low-level output	V _{CC} = MAX, See Note 2	1	45	70	mΑ

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. [‡]All typical values are at V_{CC} = 5 V, T_A = 25°C.

 $rac{8}{3}$ Not more than one output should be shorted at a time and duration of short-circuit should not exceed one second.

NOTE 2: ICCL is measured with the outputs open and all inputs grounded.

switching characteristics, VCC = 5 V, TA = 25° C

PARAMETER¶	R [¶] FROM (INPUT) (TEST CONDITIONS	MIN	TYP	MAX	UNIT
^t PLH	Data	Y	1		6	9	ns
tPHL	Data	Y]		6	9	ns
tPLH	Select	Y	CL = 15 pF, RL = 280 Ω,		11.5	18	ns
tPHL.	Select	Y	See Note 3		12	18	ns
^t PLH	Strobe G	Y			10	15	пs
трнц	Strobe G	Y	7		9	13.5	កទ

 f_{tpLH} = propagation delay time, low-to-high-level output

tpHL = propagation datay time, high-to-low-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

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18-Sep-2008

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	n MSL Peak Temp ⁽³⁾
76011012A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
7601101EA	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
7601101FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type
JM38510/07902BEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI
JM38510/07902BFA	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI
JM38510/30902B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
JM38510/30902BEA	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
SN54153J	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
SN54LS153J	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
SN54S153J	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI
SN74153N	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI
SN74LS153D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153DG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153DRG4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153J	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI
SN74LS153N	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS153N3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI
SN74LS153NE4	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS153NSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153NSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS153NSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S153D	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI
SN74S153N	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI
SN74S153N3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI
SNJ54153J	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54153W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type
SNJ54LS153FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54LS153J	ACTIVE	CDIP	J	16	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54LS153W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type
SNJ54S153FK	OBSOLETE	LCCC	FK	20		TBD	Call TI	Call TI
SNJ54S153J	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI

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Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SNJ54S153W	OBSOLETE	CFP	W	16	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

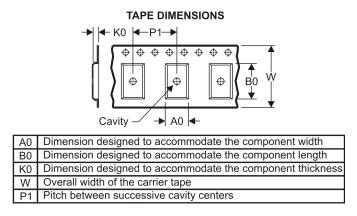
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*A	I dimensions are nominal												
	Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	SN74LS153DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
	SN74LS153NSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1



PACKAGE MATERIALS INFORMATION

19-Mar-2008



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS153DR	SOIC	D	16	2500	333.2	345.9	28.6
SN74LS153NSR	SO	NS	16	2000	346.0	346.0	33.0

MECHANICAL DATA

PLASTIC SMALL-OUTLINE PACKAGE

0,51 0,35 ⊕0,25⊛ 1,27 8 14 0,15 NOM 5,60 8,20 5,00 7,40 \bigcirc Gage Plane ₽ 0,25 7 1 1,05 0,55 0°-10° Δ 0,15 0,05 Seating Plane — 2,00 MAX 0,10PINS ** 14 16 20 24 DIM 10,50 10,50 12,90 15,30 A MAX A MIN 9,90 9,90 12,30 14,70 4040062/C 03/03

NOTES: A. All linear dimensions are in millimeters.

NS (R-PDSO-G**)

14-PINS SHOWN

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



J (R-GDIP-T**) 14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

MLCC006B - OCTOBER 1996

FK (S-CQCC-N**)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004



W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. This package can be hermetically sealed with a ceramic lid using glass frit.
 - D. Index point is provided on cap for terminal identification only.
 - E. Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC



D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AC.



N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- \triangle The 20 pin end lead shoulder width is a vendor option, either half or full width.



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