PolySwitch LVR/LVRL Line Voltage Rated Devices

PolySwitch LVR devices help protect electric motors and transformers used in commercial and home appliances from damage caused by mechanical overloads, overheating, stall, lost neutral and other potentially harmful conditions.

The PolySwitch LVR product line of polymeric positive thermal coefficient (PPTC) line voltage devices includes components that are rated for line voltages of 120 VAC and 240 VAC, for up to 2A of operating current at 20°C. They help protect against damage caused by both overcurrent surges and overtemperature faults, offer low resistance, and are compatibly sized with fuse solutions.

Unlike traditional fuses, PolySwitch devices do not require replacement after a fault event. After power has been removed and the overcurrent condition eliminated, the circuit is restored to normal operating condition. Compared to bimetal breakers, they offer greater flexibility, longer lifespan, and lower electromagnetic interference (EMI).

The PolySwitch LVR devices' resettable functionality and latching attributes make them a reliable, cost-effective circuit protection solution for both intermittent- and continuous-operation motor applications. Their low resistance, fast time-to-trip, and low profile help circuit designers provide a safe and dependable product, comply with regulatory agency requirements, and reduce warranty repair costs.

Benefits:

Reduced board space and assembly

Reduced nuisance tripping

- Reduced repair and replacement cost
- Fast time-to-trip helps reduce currentgenerated heat to motor windings and sensitive electronic components
- **Features:**
- Overcurrent and overtemperature protection in a single component
- Resettable functionality
- Latching device
- Compliant with UL 1434 safety standards



Applications:

- Continuous-operation motors
- Intermittent-operation motors
- Transformers
- Power supplies
- Fans and blowers
- Electronic controllers

Electrical Characteristics—LVR/LVRL 240V AC/120V AC

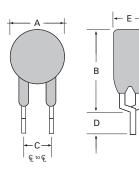
Part Number	I _{HOLD} (A)	I _{TRIP} (A)	V _{MAX} (V)	V _{MAX} Interrupt (VAC)	I _{MAX} Interrupt (A)	Power Dissipation Typ (W)	Ma Time-f (A)		R_{MIN} (Ω)	R_{MAX} (Ω)	R _{1 MAX} (Ω)	Lead Size [mm (AWG)]
LVR005K	0.05	0.12	240	265	1.0	0.7	0.25	15.0	18.50	31.00	65.00	[0.51mm (24)]
LVR005S	0.05	0.12	240	265	1.0	0.7	0.25	15.0	18.50	31.00	65.00	[0.51mm (24)]
LVR008K	0.08	0.19	240	265	1.2	0.8	0.40	15.0	7.40	12.00	26.00	[0.51mm (24)]
LVR008S	0.08	0.19	240	265	1.2	0.8	0.40	15.0	7.40	12.00	26.00	[0.51mm (24)]
LVR012K	0.12	0.30	240	265	1.2	1.0	0.60	15.0	3.00	6.50	12.00	[0.51mm (24)]
LVR012S	0.12	0.30	240	265	1.2	1.0	0.60	15.0	3.00	6.50	12.00	[0.51mm (24)]
LVR016K	0.16	0.37	240	265	2.0	1.4	0.80	15.0	2.50	4.10	7.80	[0.51mm (24)]
LVR016S	0.16	0.37	240	265	2.0	1.4	0.80	15.0	2.50	4.10	7.80	[0.51mm (24)]
LVR025K	0.25	0.56	240	265	3.5	1.5	1.25	18.5	1.30	2.10	3.80	[0.64mm (22)]
LVR025S	0.25	0.56	240	265	3.5	1.5	1.25	18.5	1.30	2.10	3.80	[0.64mm (22)]
LVR033K	0.33	0.74	240	265	4.5	1.7	1.65	21.0	0.77	1.24	2.60	[0.64mm (22)]
LVR033S	0.33	0.74	240	265	4.5	1.7	1.65	21.0	0.77	1.24	2.60	[0.64mm (22)]
LVR040K	0.40	0.90	240	265	5.5	2.0	2.00	24.0	0.60	0.97	1.90	[0.64mm (22)]
LVR040S	0.40	0.90	240	265	5.5	2.0	2.00	24.0	0.60	0.97	1.90	[0.64mm (22)]
LVR055K	0.55	1.25	240	265	7.0	3.4	2.75	26.0	0.45	0.73	1.45	[0.81mm (20)]
LVR055S	0.55	1.25	240	265	7.0	3.4	2.75	26.0	0.45	0.73	1.45	[0.81mm (20)]
LVR075S	0.75	1.5	240	265	7.5	2.6	3.75	18.0	0.32	0.48	0.84	[0.81mm (20)]
LVR100S	1.00	2.00	240	265	10.0	2.9	5.00	21.0	0.22	0.33	0.58	[0.81mm (20)]
LVR125S	1.25	2.50	240	265	12.5	3.3	6.25	23.0	0.17	0.18	0.44	[0.81mm (20)]
LVR200S	2.00	4.00	240	265	20.0	4.5	10.00	28.0	0.09	0.13	0.22	[0.81mm (20)]
LVRL075S	0.75	1.52	120	135	7.5	1.8	3.75	14.0	0.25	0.40	0.69	[0.81mm (20)]
LVRL100S	1.00	2.00	120	135	10.0	2.2	5.00	13.6	0.18	0.27	0.47	[0.81mm (20)]
LVRL125S	1.25	2.50	120	135	12.5	2.0	6.25	18.0	0.12	0.18	0.32	[0.81mm (20)]
LVRL135S	1.35	2.70	120	135	13.5	2.8	6.75	20.0	0.11	0.17	0.30	[0.81mm (20)]
LVRL200S	2.00	4.00	120	135	20.0	3.9	10.00	36.0	0.08	0.12	0.21	[0.81mm (20)]

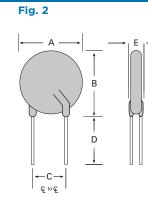


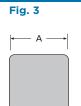
Part		Α		В	(с	0)		E	
Number	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Figure
LVR005K	_	8.3	_	12.9	4.3	5.8	7.6	_	_	3.8	
	_	(0.33)	_	(0.51)	(0.17)	(0.23)	(0.30)	_	_	(0.15)	1
LVR005S	_	8.3	_	10.7	4.3	5.8	7.6	_	_	3.8	
	_	(0.33)	_	(0.43)	(0.17)	(0.23)	(0.30)	_	-	(0.15)	2
LVR008K	_	8.3	_	12.9	4.3	5.8	7.6	—	—	3.8	1
		(0.33)		(0.51)	(0.17)	(0.23)	(0.30)	-	-	(0.15)	1
LVR008S	_	8.3 (0.33)	_	10.7 (0.43)	4.3 (0.17)	5.8 (0.23)	7.6 (0.30)	_	_	3.8 (0.15)	2
LVR012K	_	8.3	_	12.9	4.3	5.8	7.6	_	_	3.8	
EVICOLEIC	_	(0.33)	_	(0.51)	(0.17)	(0.23)	(0.30)	_	_	(0.15)	1
LVR012S	_	8.3	_	10.7	4.3	5.8	7.6	_	_	3.8	
	_	(0.33)	_	(0.43)	(0.17)	(0.23)	(0.30)	-	-	(0.15)	2
LVR016K	_	9.9	—	13.8	4.3	5.8	7.6	-	_	3.8	
	_	(0.39)	_	(0.54)	(0.17)	(0.23)	(0.30)	—	-	(0.15)	1
LVR016S	—	9.9	_	12.5	4.3	5.8	7.6	_	_	3.8	-
	_	(0.39)	_	(0.50)	(0.17)	(0.23)	(0.30)	-	_	(0.15)	2
LVR025K	—	9.6	_	18.8	4.3	5.8	7.6	-	—	3.8	3
	_	(0.38)		(0.74)	(0.17)	(0.23)	(0.30)	_		(0.15)	5
LVR025S	_	9.6 (0.38)	_	17.4 (0.69)	4.3 (0.17)	5.8 (0.23)	7.6 (0.30)	_	_	3.8 (0.15)	4
LVR033K	_	11.4	_	19.0	4.3	5.8	7.6	_	_	3.8	
LVR035K	_	(0.45)	_	(0.75)	(0.17)	(0.23)	(0.30)	_	_	(0.15)	3
LVR033S	_	11.4	_	16.5	4.3	5.8	7.6	_	_	3.8	
	_	(0.45)	_	(0.65)	(0.17)	(0.23)	(0.30)	_	_	(0.15)	4
LVR040K	_	11.5	_	20.9	4.3	5.8	7.6	_	_	3.8	
	-	(0.46)	_	(0.82)	(0.17)	(0.23)	(0.30)	-	-	(0.15)	3
LVR040S	_	11.5	—	19.5	4.3	5.8	7.6	_	_	3.8	
	_	(0.46)	_	(0.77)	(0.17)	(0.23)	(0.30)	_	_	(0.15)	4
LVR055K	_	14.0	_	22.4	4.3	5.8	7.6	-	-	4.1	-
		(0.55)	_	(0.88)	(0.17)	(0.23)	(0.30)	_	_	(0.16)	3
LVR055S	_	14.0 (0.55)	_	21.7 (0.85)	4.3 (0.17)	5.8 (0.23)	7.6 (0.30)	_	_	4.1 (0.16)	4
LVR075S	_	11.5		23.4	4.3	5.8	5.1		_	4.8	4
LVR0755	_	(0.45)		(0.92)	(0.17)	(0.23)	(0.20)		_	(0.19)	4
LVR100S	_	18.7	_	24.40	8.9	11.4	5.1	_	_	5.10	
	_	(0.74)	_	(0.96)	(0.35)	(0.45)	(0.20)	_	_	(0.20)	2
LVR125S	_	21.20	_	27.40	8.9	11.4	5.1	_	_	5.30	
	_	(0.84)	_	(1.08)	(0.35)	(0.45)	(0.20)	-	_	(0.21)	2
LVR200S	_	24.9	_	33.8	8.9	11.4	5.1	_	_	6.1	
	_	(0.98)	_	(1.33)	(0.35)	(0.45)	(0.20)	-	_	(0.24)	4
LVRL075S	-	10.9	_	17.0	4.3	5.8	7.6	_	-	4.1	4
	—	(0.43)		(0.67)	(0.17)	(0.23)	(0.30)		_	(0.16)	4
LVRL100S	_	11.5 (0.45)	_	20.1 (0.79)	4.3 (0.17)	5.8 (0.23)	7.6 (0.30)	_	_	4.1 (0.16)	4
LVRL125S	_	14.0	_	21.7	4.3	5.8	7.6	_	_	4.1	+
LVKLIZJJ	_	(0.55)	_	(0.85)	4.3 (0.17)	5.8 (0.23)	(0.30)	_	_	(0.16)	4
LVRL135S	_	16.3	_	21.7	4.3	5.8	7.6	_	_	4.1	
	_	(0.64)	_	(0.85)	(0.17)	(0.23)	(0.30)	_	_	(0.16)	4
LVRL200S	_	23.5	_	31.8	9.4	10.9	7.6	_	_	4.1	
	_	(0.93)	_	(1.25)	(0.37)	(0.43)	(0.30)	_	_	(0.16)	4

Dimensions in Millimeters (Inches)









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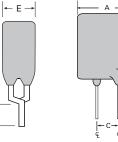
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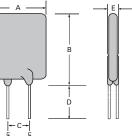
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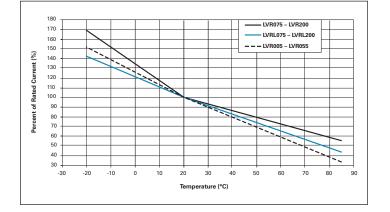
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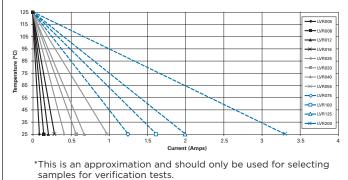




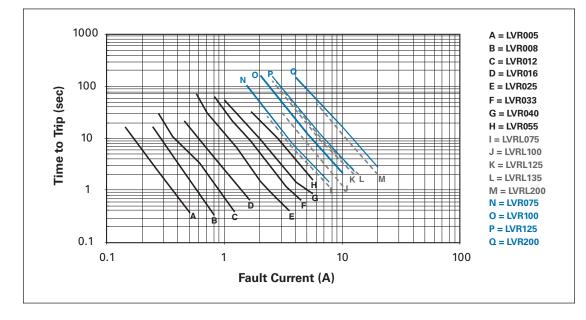
Typical Thermal Derating



Typical Trip Current vs. Temperature*



Typical Time-to-Trip at 20°C



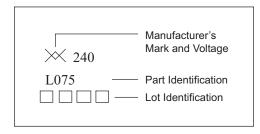
Agency Recognitions

For LVR Devices					
UL	1434 - Thermistor type devices, File # E74889				
CSA	class 9073-32 Thermistor PTC type, File # CA78165C				
ΤÜV	Certificate number available on request (per IEC 60730-1).				

Part Numbering System

LVR 075 S -2	
	Blank = Packaged in bags -1 = 25.4mm (1.0 inch) minimum lead length -2 = Tape and reel -AP = Ammo pack -X.X = Special lead cut length (inch) Modifier K = Standard kinked lead B = Special kinked lead S = Straight lead U = Uncoated device Hold current indicator Product series LVR = 240V rated
	LVRL = 120V rated

Marking



www.circuitprotection.com

Physical Characteristics					
LVR005-016: Tin-plated copper, 0.205mm ² (24AWG), ø0.51mm (0.020in.) LVR025-040: Tin-plated copper, 0.32mm ² (22AWG), ø0.64mm (0.025in.) LVR055-200: Tin-plated copper, 0.52mm ² (20AWG), ø0.81mm (0.032in.) LVRL: Tin-plated copper, 0.52mm ² (20AWG), ø0.81mm (0.032in.)					
LVR005-055: Cured flame retardant epoxy polymer, meets UL94V-0 LVR075-200: Cured flame retardant modified silicone meets UL94V-0 LVRL: Cured flame retardant epoxy polymer, meets UL94V-0					
Solderability pre ANSI/J-STD-002 Category 3					
Per IEC-STD 68-2-20, Test Tb, Method 1A, Condition B, can withstand 10 seconds at 260°C \pm 5°C					

Devices are not designed to be placed through a reflow process

Environmental Characteristics

Test	Conditions	Resistance Change	
Passive aging	70°C, 1000 hours	±5%	
	85°C, 1000 hours	±5%	
Humidity aging	85°C, 85%RH, 1000 hours	±5%	
Thermal Shock	85°C, -40°C (10 times)	±5%	
Solvent resistance	MIL-STD-202, Method 215F	No change	

WARNING: Application Limitations for the LVR Product Line

- Users should independently evaluate the suitability of and test each product selected for their own application.
- 2) This product should not be used in an application where the maximum interrupt voltage or maximum interrupt current can be exceeded in a fault condition. Operation beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- 3) A PPTC device is not a fuse—it is a nonlinear thermistor that limits current. Because under a fault condition all PPTC devices go into a high resistance state but not open circuit, hazardous voltage may be present at PPTC locations.
- 4) The devices are intended for protection against occasional overcurrent or overtemperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- 5) In most applications, power must be removed and the fault condition cleared in order to reset a PPTC device. However, under certain unusual conditions, a PPTC device may automatically reset. Accordingly, in applications where an automatic reset could create a safety hazard, such as garbage disposals and blenders, appropriate qualification testing should be performed.

- 6) It is the responsibility of the user to determine the need for back-up or fail safe protection to prevent damage that may occur in the event of abnormal function or failure of the PPTC device.
- 7) Operation in circuits with a large inductance can generate a circuit voltage (Ldi/dt) above the rated voltage of a PPTC device.
- 8) Devices are not recommended for reflow soldering.
- Device performance can be negatively impacted if devices are handled in a manner inconsistent with recommended electronic, thermal, or mechanical procedures for electronic components.
- 10) PPTC devices are not recommended to be installed in applications where the device is constrained such that its PPTC properties are inhibited, for example in rigid potting materials or in rigid housings, which lack adequate clearance to accommodate device expansion.
- Contamination of the PPTC material with certain silicone-based oils or some aggressive solvents can adversely impact the performance of the devices.

www.circuitprotection.com

www.circuitprotection.com.hk (Chinese)

www.circuitprotection.jp (Japanese)

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