

## Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead, Halogen and Antimony Free, RoHS Compliant (Note 3)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

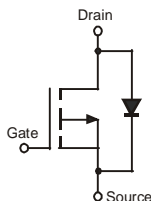
## Mechanical Data

- Case: SOT-23
- Case Material: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish).
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)

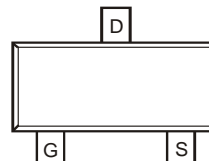
SOT-23



Top View



Equivalent Circuit



Top View

## Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	$V_{DSS}$	-50	V
Drain-Gate Voltage $R_{GS} \leq 20\text{K}\Omega$	$V_{DGR}$	-50	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (Note 1)	$I_D$	-130	mA
Pulsed Drain Current	$I_{DM}$	-1.2	A

## Thermal Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	$P_D$	300	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

## Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 2)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-50	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	-15	$\mu\text{A}$	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$
		—	—	-60	$\mu\text{A}$	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
		—	—	-100	nA	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$
Gate-Body Leakage	$I_{GSS}$	—	—	$\pm 10$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 2)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	-0.8	—	-2.0	V	$V_{DS} = V_{GS}, I_D = -1\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	—	10	$\Omega$	$V_{GS} = -5\text{V}, I_D = -0.100\text{A}$
Forward Transconductance	$g_{FS}$	0.05	—	—	S	$V_{DS} = -25\text{V}, I_D = -0.1\text{A}$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{iss}$	—	—	45	pF	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	—	—	25	pF	
Reverse Transfer Capacitance	$C_{rss}$	—	—	12	pF	
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{D(ON)}$	—	10	—	ns	$V_{DD} = -30\text{V}, I_D = -0.27\text{A}, R_{GEN} = 50\Omega, V_{GS} = -10\text{V}$
Turn-Off Delay Time	$t_{D(OFF)}$	—	18	—	ns	

- Notes:
1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
  2. Short duration pulse test used to minimize self-heating effect.
  3. No purposefully added lead. Halogen and Antimony Free.
  4. Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or  $\text{Sb}_2\text{O}_3$  Fire Retardants.

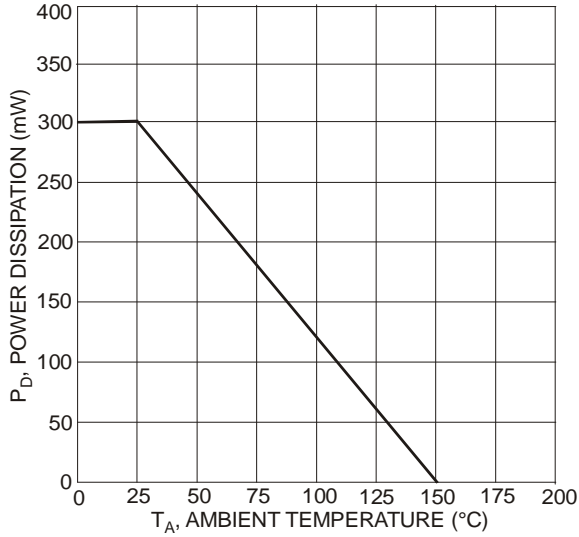


Fig. 1 Max Power Dissipation vs. Ambient Temperature

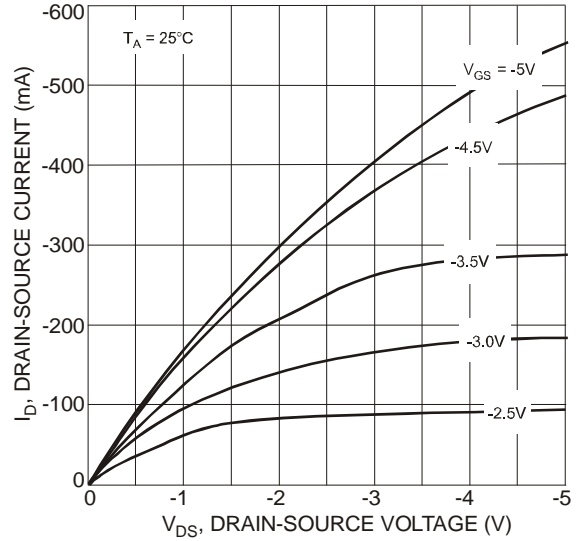


Fig. 2 Drain-Source Current vs. Drain-Source Voltage

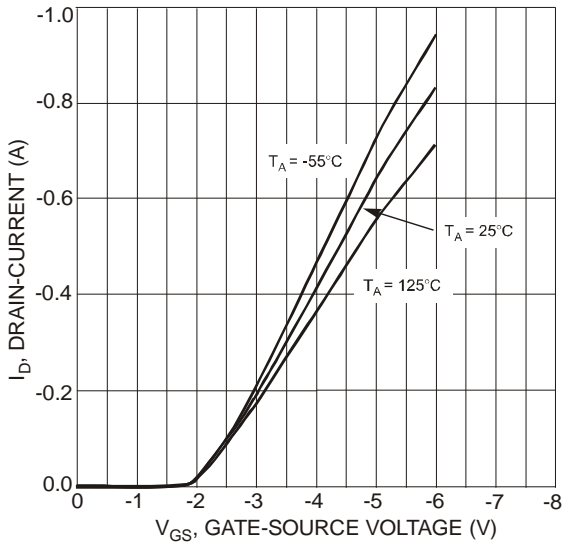


Fig. 3 Drain-Current vs. Gate-Source Voltage

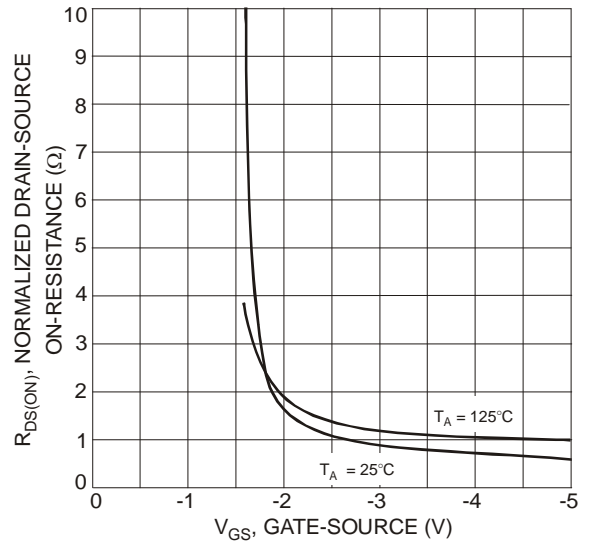


Fig. 4 On-Resistance vs. Gate-Source Voltage

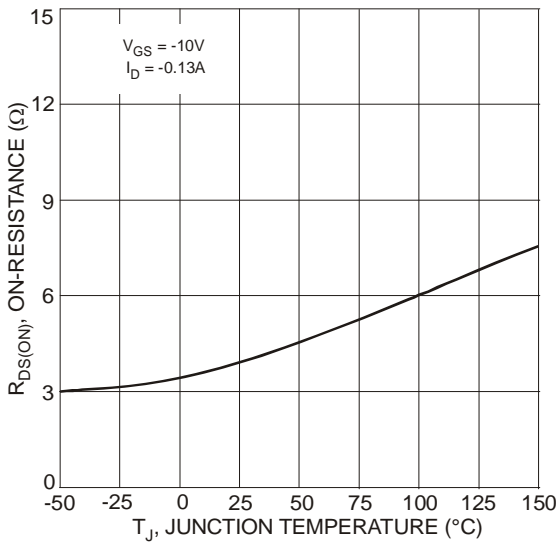


Fig. 5 On-Resistance vs. Junction Temperature

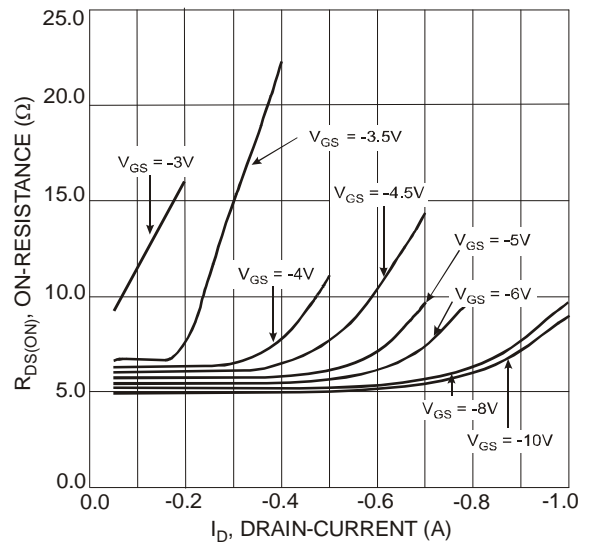


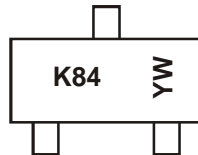
Fig. 6 On-Resistance vs. Drain-Current

**Ordering Information** (Note 5)

Part Number	Case	Packaging
BSS84-7-F	SOT-23	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**



K84 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: N = 2002)  
 M = Month (ex: 9 = September)

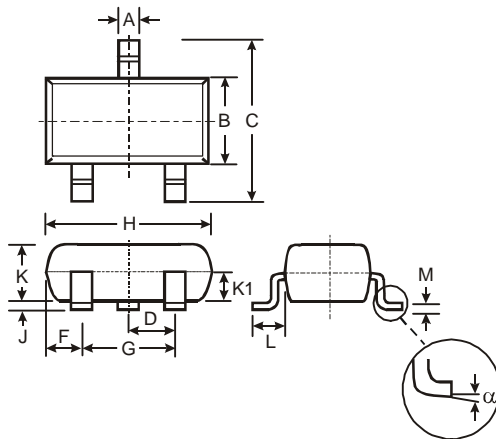
Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	K	L	M	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

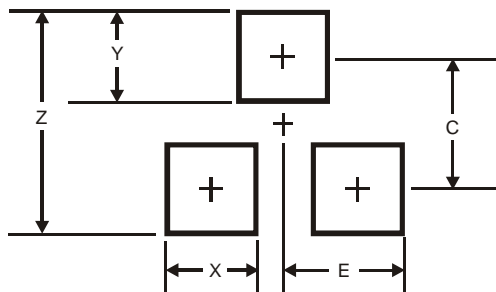
**Package Outline Dimensions**



SOT-23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
$\alpha$	0°	8°	-

All Dimensions in mm

**Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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