



P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- **ESD Protected Up To 3KV**
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

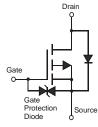
- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.006 grams (approximate)

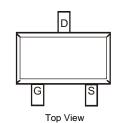




Top View







Equivalent Circuit

Maximum Ratings @T_A = 25°C unless otherwise specified

Char	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	-20	V		
Gate-Source Voltage			V_{GSS}	±6	V
Continuous Drain Current (Note 3)	Steady State	T _A = 25°C T _A = 85°C	I _D	-0.82 -0.54	Α
Pulsed Drain Current (Note 4)			I _{DM}	-6	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P _D	0.31	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 3)	$R_{\theta JA}$	398	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 3. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 4. Repetitive rating, pulse width limited by junction temperature.

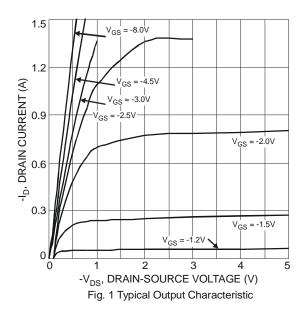


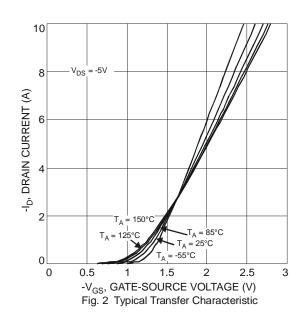
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 5)									
Drain-Source Breakdown Voltage	BV _{DSS}	-20	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$			
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	-100	nA	$V_{DS} = -20V, V_{GS} = 0V$			
Gate-Source Leakage	I _{GSS}	-		±2.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 5)									
Gate Threshold Voltage	V _{GS(th)}	-0.5	-	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$			
			0.5	0.75		$V_{GS} = -4.5V, I_D = -430mA$			
Static Drain-Source On-Resistance	R _{DS (ON)}	-	0.7	1.05	Ω	$V_{GS} = -2.5V, I_D = -300mA$			
			1.0	1.5		$V_{GS} = -1.8V, I_D = -150mA$			
Forward Transfer Admittance	Y _{fs}	-	0.9		S	$V_{DS} = -10V, I_D = -250mA$			
Diode Forward Voltage	V_{SD}		-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -150mA$			
DYNAMIC CHARACTERISTICS (Note 6)									
Input Capacitance	Ciss	-	59.76	-	pF	10)/)/			
Output Capacitance	Coss	-	12.07	-	pF	$V_{DS} = -16V, V_{GS} = 0V,$ -f = 1.0MHz			
Reverse Transfer Capacitance	C _{rss}	-	6.36	-	pF	1 = 1.000112			
Total Gate Charge	Q_g	-	622.4	-	рC	V 45V V 40V			
Gate-Source Charge	Q_{gs}	-	100.3	-	рC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -250 \text{mA}$			
Gate-Drain Charge	Q_{gd}	-	132.2	-	рC	ID = -250IIIA			
Turn-On Delay Time	t _{D(on)}	-	5.1	-	ns	101/11/			
Turn-On Rise Time	t _r	-	8.1	-	ns	$V_{DD} = -10V, V_{GS} = -4.5V,$			
Turn-Off Delay Time	t _{D(off)}	-	28.4	-	ns	$R_L = 47\Omega$, $R_G = 10\Omega$, $I_D = -200$ mA			
Turn-Off Fall Time	t _f	-	20.7	-	ns	1D = -200111A			

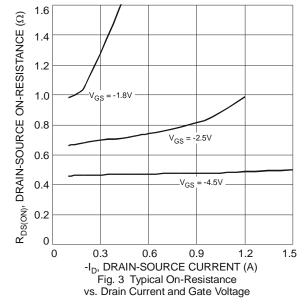
Notes:

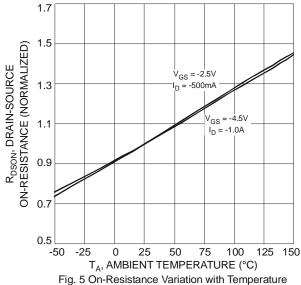
- 5. Short duration pulse test used to minimize self-heating effect.
- 6. Guaranteed by design. Not subject to production testing.











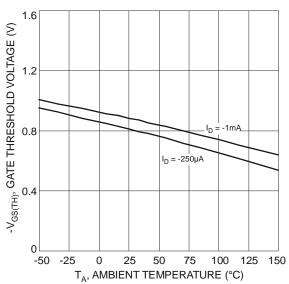
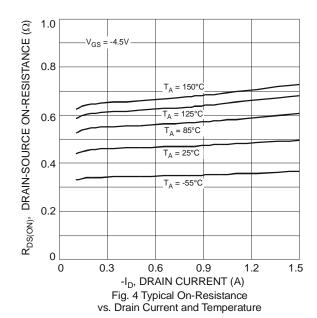


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



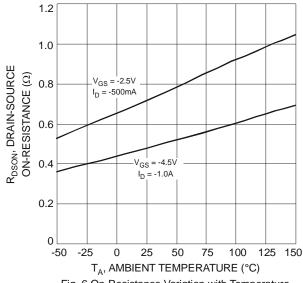
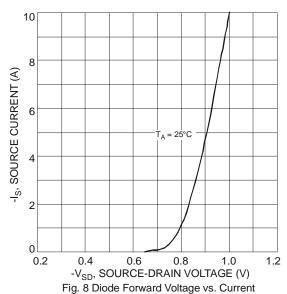
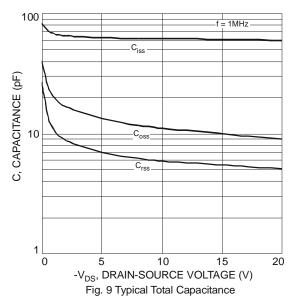


Fig. 6 On-Resistance Variation with Temperature







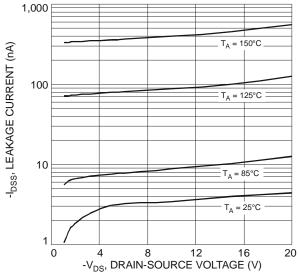
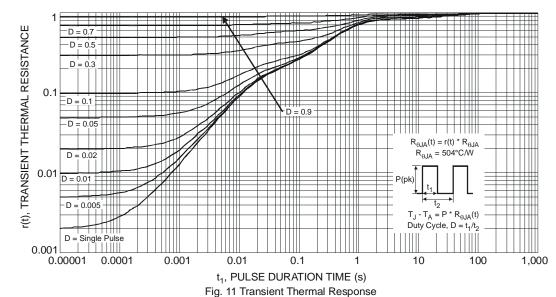


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

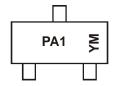


Ordering Information (Note 7)

Part Number	Case	Packaging
DMG1013UW-7	SOT-323	3000 / Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



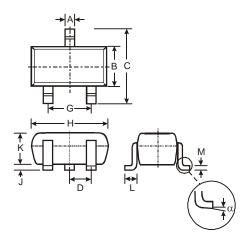
PA1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Year	2008	20	009	2010	2	2011	2012		2013	2014		2015
Code	V		W	Χ		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

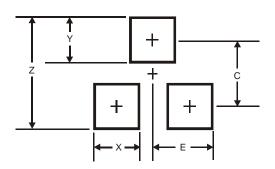


Package Outline Dimensions



SOT-323						
Dim	Min	Max	Тур			
Α	0.25	0.40	0.30			
В	1.15	1.35	1.30			
С	2.00	2.20	2.10			
D	-	-	0.65			
G	1.20	1.40	1.30			
Н	1.80	2.20	2.15			
J	0.0	0.10	0.05			
K	0.90	1.00	1.00			
L	0.25	0.40	0.30			
M	0.10	0.18	0.11			
α	0°	8°	-			
All	All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Y	0.9
С	1.9
E	1.0



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2009, Diodes Incorporated

www.diodes.com