International TOR Rectifier

HFA15TB60 HFA15TB60-1

HEXFRED™

Ultrafast, Soft Recovery Diode

Features

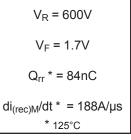
- Ultrafast Recovery
- · Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- · Specified at Operating Conditions

Benefits

- · Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- · Higher Frequency Operation
- · Reduced Snubbing
- · Reduced Parts Count

Description

International Rectifier's HFA15TB60 is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 volts and 8 amps per Leg continuous current, the HFA15TB60 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA15TB60 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.







Absolute Maximum Ratings

	Parameter	Max	Units	
V_R	Cathode-to-Anode Voltage	600	V	
I _F @ T _C = 100°C	Continuous Forward Current	15		
I _{FSM}	Single Pulse Forward Current	150	A	
I _{FRM}	Maximum Repetitive Forward Current	60	1	
P _D @ T _C = 25°C	Maximum Power Dissipation	74	100	
P _D @ T _C = 100°C	Maximum Power Dissipation	29	- w	
TJ	Operating Junction and	55 1- :450		
T _{STG}	Storage Temperature Range	- 55 to +150	С	

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions	
V_{BR}	Cathode Anode Breakdown Voltage	600			V	I _R = 100μA	
V _{FM}	Max Forward Voltage		1.3	1.7	V	I _F = 15A	
			1.5	2.0		I _F = 30A See Fig.	1
			1.2	1.6		I _F = 15A, T _J = 125°C	
I _{RM}	Max Reverse Leakage Current		1.0	10		$V_R = V_R$ Rated See Fig.	2
			400	1000	μA	$T_J = 125$ °C, $V_R = 0.8 \times V_R$ Rated	
C _T	Junction Capacitance		25	50	pF	$V_R = 200V$ See Fig.	3
L _S	Series Inductance		8.0			Measured lead to lead 5mm from	
					nH	package body	

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions	
t _{rr}	Reverse Recovery Time		19			$I_F = 1.0A$, $di_f/dt = 200A$	Vμs, V _R = 30V
t _{rr1}	See Fig. 5		42	60	ns	T _J = 25°C	
t _{rr2}	•		74	120		T _J = 125°C	I _F = 15A
I _{RRM1}	Peak Recovery Current		4.0	6.0	Α	T _J = 25°C	
I _{RRM2}	See Fig. 6		6.5	10	^	T _J = 125°C	$V_{R} = 200V$
Q _{rr1}	Reverse Recovery Charge		84	180	nC	T _J = 25°C	
Q _{rr2}	See Fig. 7		241	600	110	T _J = 125°C	$di_f/dt = 200A/\mu s$
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current		188		A/us	T _J = 25°C	
di _{(rec)M} /dt2	During t _b See Fig. 8		160	-	Α/μδ	T _J = 125°C	

Thermal - Mechanical Characteristics

	Parameter	Min	Тур	Max	Units
T _{lead} ①	Lead Temperature			300	°C
R _{th} JC	Thermal Resistance, Junction to Case			1.7	
R _{thJA} ②	Thermal Resistance, Junction to Ambient			80	K/W
RthCS 3	Thermal Resistance, Case to Heat Sink		0.5		
Wt	Weight		2.0		g
	Weight		0.07		(oz)
Т	Mounting Torque	6.0		12	Kg-cm
	Wounting Forque	5.0		10	lbf•in

① 0.063 in. from Case (1.6mm) for 10 sec

② Typical Socket Mount

3 Mounting Surface, Flat, Smooth and Greased

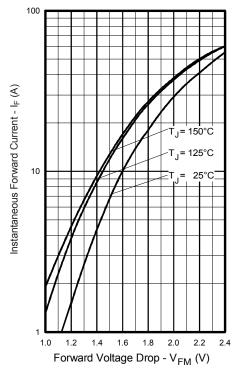


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

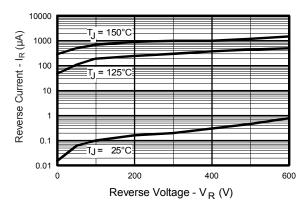


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

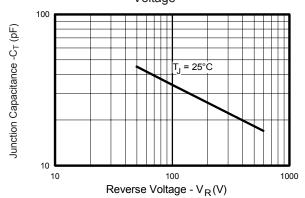


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

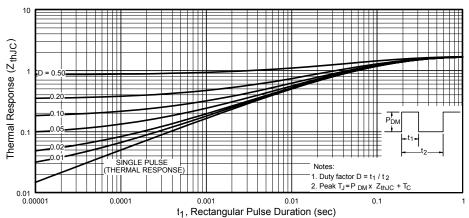


Fig. 4 - Maximum Thermal Impedance Zthic Characteristics

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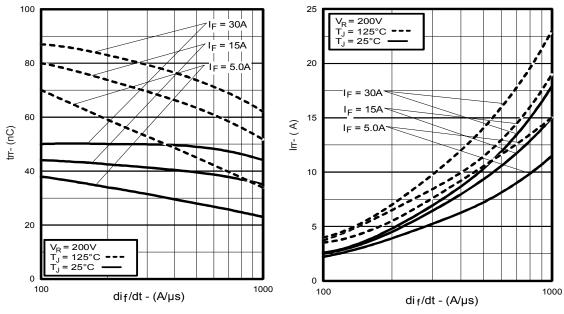


Fig. 5 - Typical Reverse Recovery vs. di_f/dt

Fig. 6 - Typical Recovery Current vs. di_f/dt

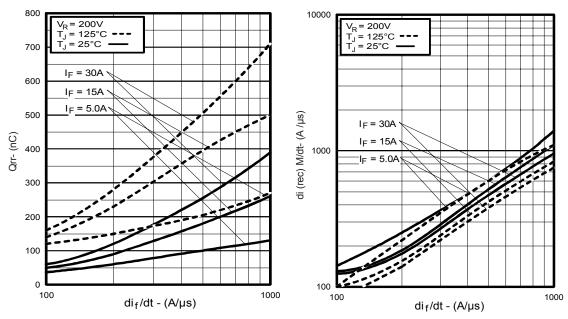


Fig. 7 - Typical Stored Charge vs. di_f/dt

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Fig. 8 - Typical $di_{(rec)M}/dt$ vs. di_f/dt

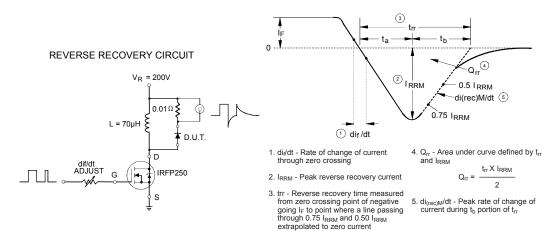
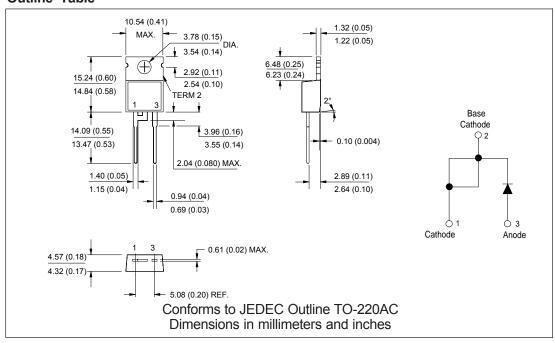


Fig. 9 - Reverse Recovery Parameter Test Circuit

Fig. 10 - Reverse Recovery Waveform and Definitions

Outline Table

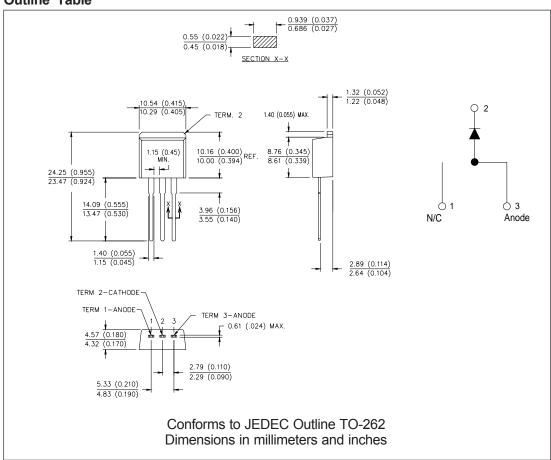


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Outline Table

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Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level. Qualification Standards can be found on IR's Web site.



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