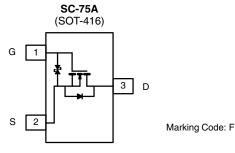


Vishay Siliconix

N-Channel 60 V (D-S) MOSFET

| PRODUCT SUMMARY | | | | | | |
|---------------------------|--|-------------------------|---------------------|--|--|--|
| V _{DS(min.)} (V) | R_{DS(on)} (Ω) | V _{GS(th)} (V) | I _D (mA) | | | |
| 60 | 1.25 at V _{GS} = 10 V | 1 to 2.5 | 330 | | | |



Ordering Information: Si1022R-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs
- Low On-Resistance: 1.25 Ω
- Low Threshold: 2.5 V
- Low Input Capacitance: 30 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- Miniature Package
- ESD Protected: 2000 V
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid State Relays

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Error Voltage
- Small Board Area

| ABSOLUTE MAXIMUM RATINGS | S (T _A = 25 °C, un | less otherwise n | oted) | | |
|--|--------------------------------------|-----------------------------------|-------------|-------|--|
| Parameter | | Symbol | Limit | Unit | |
| Drain-Source Voltage | | V _{DS} | 60 | V | |
| Gate-Source Voltage | | V _{GS} | ± 20 | v | |
| | T _A = 25 °C | 1- | 330 | | |
| Continuous Drain Current ^a | T _A = 85 °C | D | 240 | mA | |
| Pulsed Drain Current ^a | | I _{DM} 650 | | | |
| | T _A = 25 °C | P _D | 250 | mW | |
| Power Dissipation ^a | T _A = 85 °C | | 130 | 11100 | |
| Thermal Resistance, Maximum Junction-to-Ambienta | | R _{thJA} | 500 | °C/W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | °C | |

Notes:

a. Surface mounted on FR4 board, power applied for t \leq 10 s.



COMPLIANT HALOGEN

FREE

Si1022R

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|---------------------|---|------|------|-------|------|--|
| Static | | | | • | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 V, I_D = 10 \mu A$ | 60 | | | v | |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = 0.25 \text{ mA}$ | 1 | | 2.5 | v | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 10 V$ | | | ± 150 | | |
| | | T _J = 85 °C | | | ± 500 | 1 | |
| | | $V_{DS} = 0 V, V_{GS} = \pm 5 V$ | | | ± 20 | nA | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | | | 10 | | |
| | | T _J = 85 °C | | | 100 | | |
| | | $V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | | | 1 | μA | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$ | 500 | | | mA | |
| | | V _{DS} = 7.5 V, V _{GS} = 10 V | 800 | | | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 200 \text{ mA}$ | | | 3.0 | 1 | |
| | | T _J = 125 °C | | | 5.0 | Ω | |
| | | V _{GS} = 10 V, I _D = 500 mA | | | 1.25 | 52 | |
| | | T _J = 125 °C | | | 2.25 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 10 V, I _D = 200 mA | 100 | | | mS | |
| Diode Forward Voltage ^a | V _{SD} | V _{GS} = 0 V, I _S = 200 mA | | | 1.3 | V | |
| Dynamic ^b | • | | | | | | |
| Input Capacitance | C _{iss} | | | 30 | | pF | |
| Output Capacitance | C _{oss} | $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | 6 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 2.5 | | | |
| Gate Charge | Qg | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 250 \text{ mA}, \text{ V}_{GS} = 4.5 \text{ V}$ | | | 0.6 | nC | |
| Switching ^{b, c} | - | · | - | • | | | |
| Turn-On Time | t _(on) | V _{DD} = 30 V, R _L = 150 Ω, | | | 25 | - | |
| Turn-Off Time | t _(off) | $I_{\rm D}$ = 200 mA, $V_{\rm GEN}$ = 10 V, $R_{\rm q}$ = 10 Ω | | | 35 | ns | |

Notes:

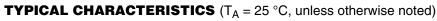
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

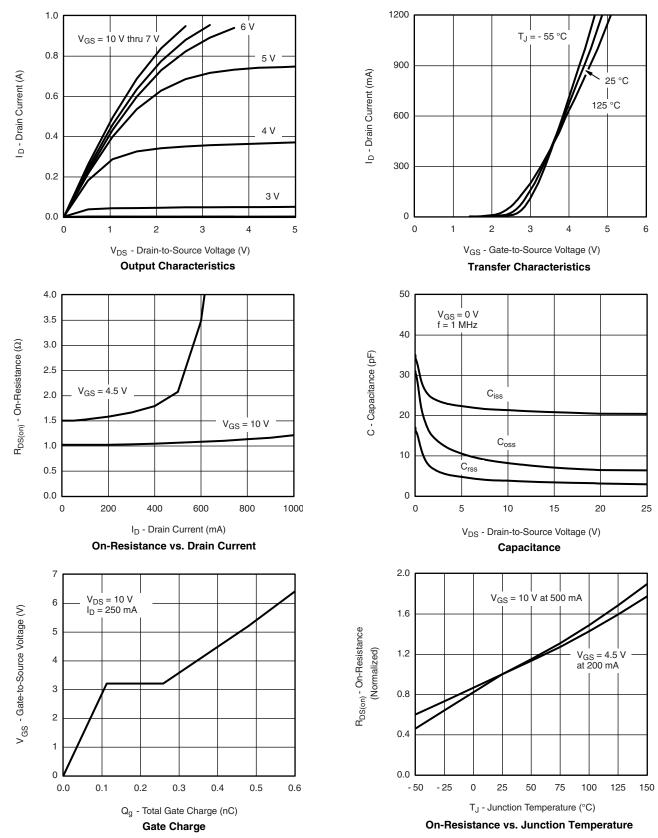
b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







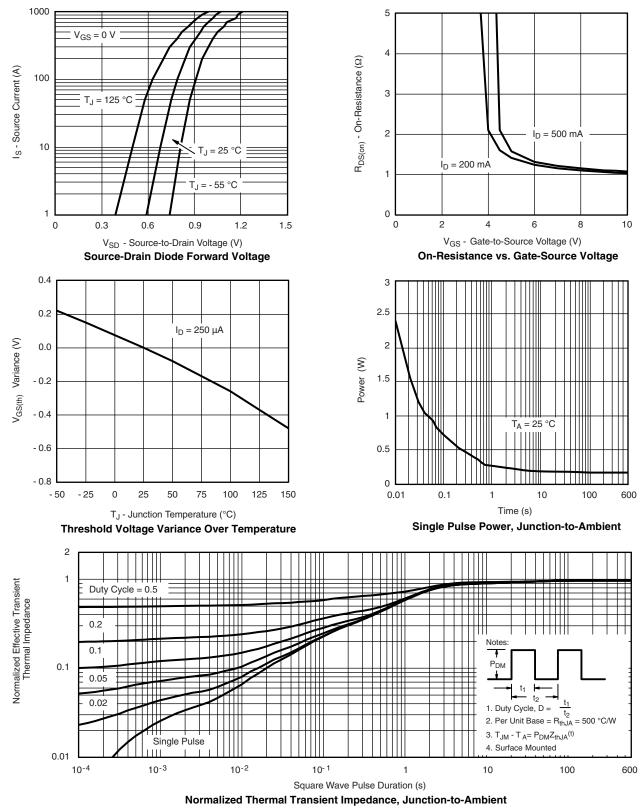
Document Number: 71331 S10-2432-Rev. E, 25-Oct-10

Si1022R

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71331.



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