



### LOW V<sub>CE(SAT)</sub> NPN SURFACE MOUNT TRANSISTOR

SOT-563

1, 2, 5, 6

6

5 4

2

### Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Type Available (DPLS160V)
- Surface Mount Package Suited for Automated Assembly
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 1)
- "Green Device" (Note 2)
- Qualified to AEC-Q 101 Standards for High Reliability

# **Mechanical Data**

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL • Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D •
- Terminals: Finish Matte Tin annealed over Copper leadframe. . Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.003 grams (approximate)

### **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	80	V
Collector-Emitter Voltage	V <sub>CEO</sub>	60	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Collector Current - Continuous	lc	1	A
Peak Pulse Collector Current	I <sub>CM</sub>	2	A
Base Current (DC)	Ι <sub>Β</sub>	300	mA

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^{\circ}C$	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 3) @ $T_A = 25^{\circ}C$	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 1. No purposefully added lead.

Diode's Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php. 2.

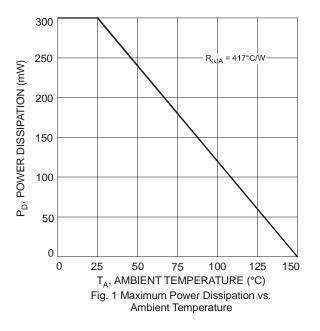
3. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

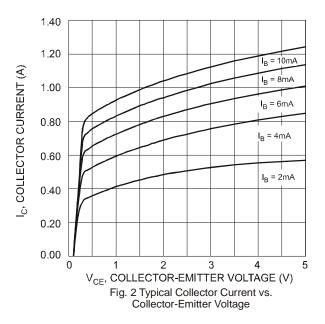


# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

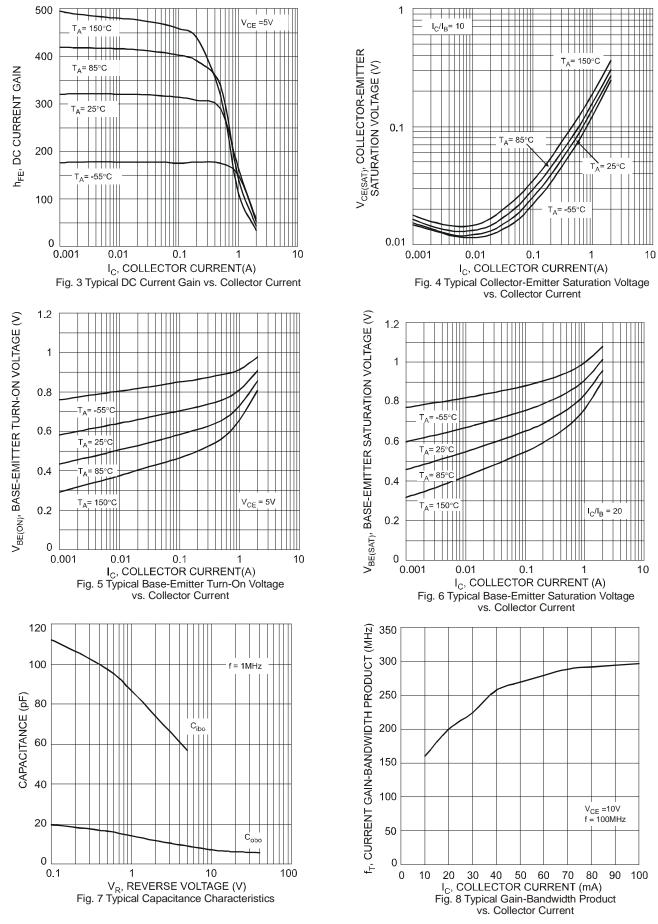
			-			
	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4) Collector-Base Breakdown Voltage	N/	80			V	100
,	V <sub>(BR)CBO</sub>	60	—	_	V	$I_{\rm C} = 100\mu A, I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>		_	_		$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	5	—		V	$I_{E} = 100 \mu A, I_{C} = 0$
Collector Cutoff Current	I <sub>CBO</sub>	—	—	100 50	nA μA	$V_{CB} = 60V, I_E = 0$ $V_{CB} = 60V, I_E = 0, T_A = 150^{\circ}C$
Collector Cutoff Current	I <sub>CES</sub>			100	nA	$V_{CE} = 60V, V_{BE} = 0$
Emitter Cutoff Current	I <sub>EBO</sub>			100	nA	$V_{EB} = 5V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)						·
DC Current Gain	h <sub>FE</sub>	250 200 100	320 280 165		V	$V_{CE} = 5V, I_C = 1mA$ $V_{CE} = 5V, I_C = 500mA$ $V_{CE} = 5V, I_C = 1A$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		80 80 140	110 140 250	mV	$I_{C} = 100$ mA, $I_{B} = 1$ mA $I_{C} = 500$ mA, $I_{B} = 50$ mA $I_{C} = 1$ A, $I_{B} = 100$ mA
Collector-Emitter Saturation Resistance	R <sub>CE(SAT)</sub>		140	250	mΩ	$I_{C} = 1A, I_{B} = 100 \text{mA}$
Base-Emitter Saturation Voltage	VBE(SAT)	_	0.91	1.1	V	$I_{C} = 1A, I_{B} = 50mA$
Base-Emitter Turn On Voltage	V <sub>BE(ON)</sub>	_	0.81	0.9	V	$V_{CE} = 5V, I_{C} = 1A$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C <sub>obo</sub>		7	10	pF	V <sub>CB</sub> = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	fT	150	270	_	MHz	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 100MHz
SWITCHING CHARACTERISTICS						·
Turn-On Time	t <sub>on</sub>		90		ns	
Delay Time	t <sub>d</sub>		17		ns	
Rise Time	tr	_	73	_	ns	$V_{CC} = 10V$
Turn-Off Time	t <sub>off</sub>		300		ns	$I_{C} = 0.5A, I_{B1} = I_{B2} = 25mA$
Storage Time	ts	_	220	_	ns	
Fall Time	t <sub>f</sub>	_	80		ns	

Notes: 4. Measured under pulsed conditions. Pulse width =  $300\mu s$ . Duty cycle  $\leq 2\%$ .









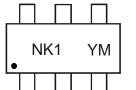


### Ordering Information (Note 5)

Device	Packaging	Shipping
DNLS160V-7	SOT-563	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**

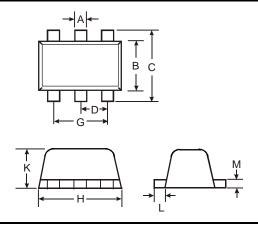


NK1 = Product Type Marking Code YM = Date Code Marking Y = Year ex: V = 2008 M = Month ex: 9 = September

Date Code Kev

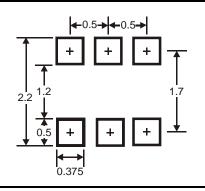
Date Code Key												
Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Y	Z		А	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

## **Package Outline Dimensions**



SOT-563							
Dim	Min Max Typ						
Α	0.15	0.30	0.20				
в	1.10	1.25	1.20				
С	1.55	1.70	1.60				
D	-	-	0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
Κ	0.55	0.60	0.60				
L	0.10	0.30	0.20				
Μ	0.10	0.18	0.11				
All Dimensions in mm							

## Suggested Pad Layout (in mm)



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