



High junction temperature Transil™

Features

- Halogen-free package
- Peak pulse power:
 - 600 W (10/1000 μs)
 - 4 kW (8/20 µs)
- Stand off voltage: 5, 12 or 13 V
- Unidirectional type
- Low clamping voltage versus standard series
- Low leakage current, 0.2 µA at 25 °C
- Operating T_i max: 175 °C
- JEDEC registered package outline

Complies with the following standards

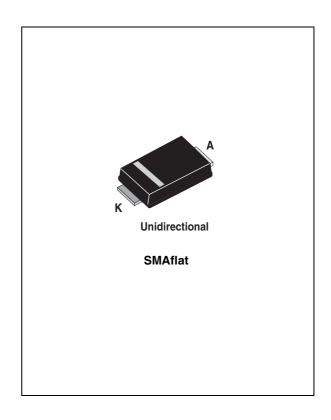
- IEC 61000-4-2 level 4:
 - 15 kV (air discharge)
 - 8 kV (contact discharge)
- MIL STD 883G-Method 3015-7: class3B
 - 25 kV (human body model)

Description

The SMA6F Transil series has been designed to protect sensitive equipment against electro-static discharges according to IEC 61000-4-2, MIL STD 883 Method 3015, and electrical over stress such as IEC 61000-4-4 and 5. They are generally for surges below 600 W 10/1000 µs.

This planar technology makes it compatible with high-end equipment and SMPS where low leakage current and high junction temperature are required to provide reliability and stability over time. Their low clamping voltages provides a better safety margin to protect sensitive circuits with extended life time expectancy.

Packaged in SMAflat non exposed pad, this minimizes PCB space consumption (footprint in accordance with IPC 7531 standard).



TM: Transil is a trademark of STMicroelectronics

Characteristics SMA6F

1 Characteristics

Table 1. Absolute ratings ($T_{amb} = 25 \, ^{\circ}C$)

Symbol	Parameter	Value	Unit	
P_PP	Peak pulse power dissipation ⁽¹⁾	T_j initial = T_{amb}	600	W
Р	Power dissipation on infinite heatsink	6	W	
I _{FSM}	Non repetitive surge peak forward current for unidirectional types	60	Α	
T _{stg}	Storage temperature range	-65 to +175	°C	
T _j	Operating junction temperature range	-55 to +175	°C	
T _L	Maximum lead temperature for soldering during 10 s	260	°C	

^{1.} For a surge greater than the maximum values, the diode will fail in short-circuit.

Table 2. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th (j-l)}	Junction to leads	20	°C/W

Table 3. Electrical characteristics - definitions ($T_{amb} = 25$ °C)

Symbol	Parameter	14 .
V _{RM}	Stand-off voltage]
V _{BR}	Breakdown voltage	'F
V _{CL}	Clamping voltage]
I _{RM}	Leakage current @ V _{RM}	V _{CL} V _{BR} V _{RM} V _F V
I _{PP}	Peak pulse current	I _R
αΤ	Voltage temperature coefficient	
V_{F}	Forward voltage drop	I _{PP}
R_D	Dynamic resistance	

SMA6F Characteristics

Туре	I _{RM} I	I _{RM} max@V _{RM}		V _{BR} @I _R ⁽¹⁾			V _{CL} @I _{PP} 10/1000 μs		R _D ⁽²⁾ 10/1000 μs	V _{CL} @I _{PP} s 8/20 μs		R _D ⁽²⁾ 8/20 μs	α T⁽³⁾	
	25 °C	85 °C		min	typ	max		max			max			max
	μΑ (Max)	٧		٧		mA	٧	Α	Ω	٧	Α	Ω	10-4/°C
SMA6F5.0A	10	50	5.0	6.40	6.74	7.07	10	9.2	68	0.029	13.4	298	0.021	5.7

1

18.5

20.4

31

29

22.9

23.9

0.135

0.154

157

147

0.055

0.054

7.8

8.3

Table 4. Electrical characteristics - values ($T_{amb} = 25$ °C)

1. Pulse test: t_p <50ms.

SMA6F12AVCL

SMA6F13A

2. To calculate maximum clamping voltage at other surge currents, use the following formula $V_{CLmax} = R_D \ x \ I_{PP} + V_{BRmax}$

13.2

14.4

13.7

15.2

14.3

15.9

3. To calculate $V_{\mbox{\footnotesize{BR}}}$ versus junction temperature, use the following formula:

12

13

 $V_{BR} @ T_{j} = V_{BR} @ 25 \ ^{\circ}C \ x \ (1 + \alpha T \ x \ (T_{j} - 25))$

0.2

0.2

1

Figure 1. Definition of lpp pulse

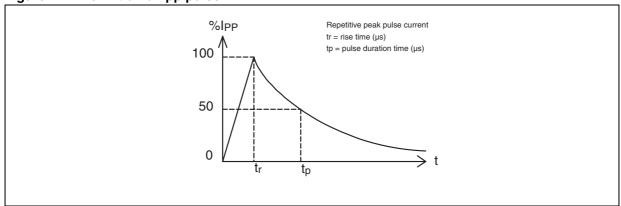
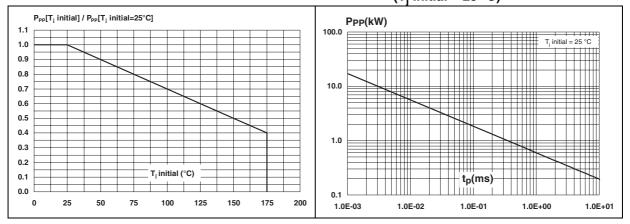


Figure 2. Relative peak power dissipation versus initial junction temperature

Figure 3. Peak pulse power versus exponential pulse duration $(T_i initial = 25 °C)$



Characteristics SMA6F

Figure 4. Clamping voltage versus peak pulse current (exponential waveform, maximum values)

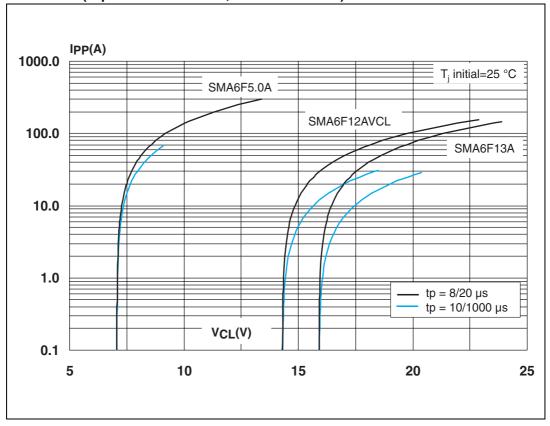
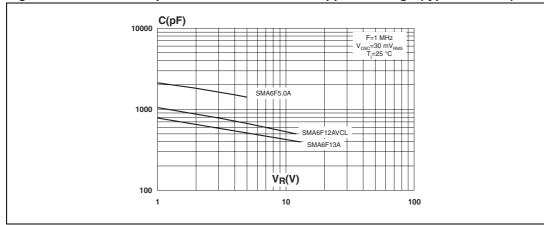


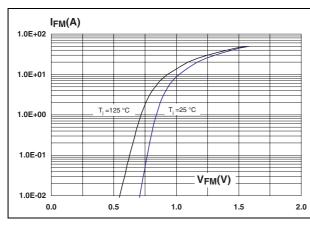
Figure 5. Junction capacitance versus reverse applied voltage (typical values)



SMA6F Characteristics

Figure 6. Peak forward voltage drop versus peak forward current (typical values)

Figure 7. Relative variation of thermal impedance junction to ambient versus pulse duration (printed ciruit board FR4, $S_{Cu} = 1 \text{ cm}^2$)



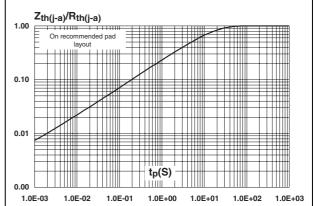
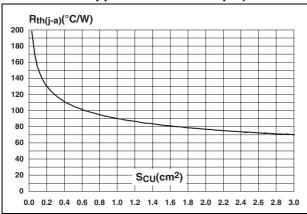
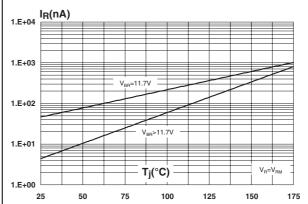


Figure 8. Thermal resistance junction to ambient versus copper surface under each lead (printed circuit board FR4, copper thickness = 35 µm)

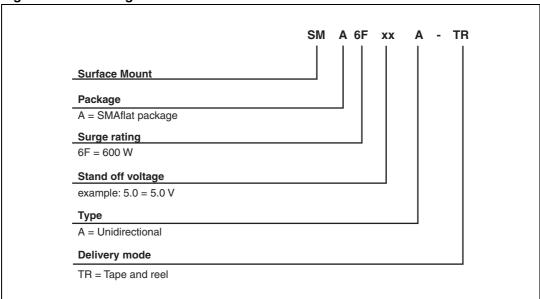
Figure 9. Leakage current versus junction temperature (typical values)





2 Ordering information scheme

Figure 10. Ordering information scheme



SMA6F Package information

3 Package information

Case: JEDEC DO-221AC molded plastic over Planar junction

• Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Band indicates cathode

Flammability: Epoxy rated UL94V-0

RoHS package

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Table 5. SMAflat (non exposed pad) dimensions

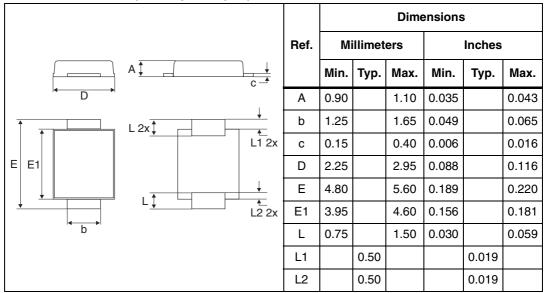
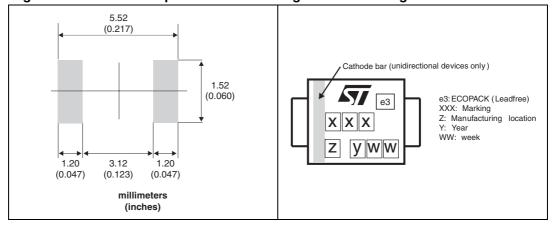


Figure 11. SMAflat footprint dimensions Figure 12. Marking information



Ordering information SMA6F

4 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
SMA6F5.0A-TR	SUA				
SMA6F12AVCL-TR	SUJ	SMAflat	0.035 g	10000	Tape and reel
SMA6F13A-TR	SUG				

5 Revision history

Table 7. Document revision history

Date	Revision	Changes				
04-Sep-2008	1	First issue.				

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