DFLR1200 / DFLR1400 / DFLR1600



1.0A SURFACE MOUNT GLASS PASSIVATED RECTIFIER PowerDI 123

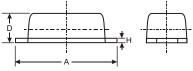
Features

Qualified to AEC-Q101 Standards for High Reliability

Ideally Suited for Automated Assembly

Green Molding Compound (No Br, Sb)

Lead Free Finish, RoHS Compliant (Note 2)



Mechanical Data

Case: PowerDI 123

Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating

94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

Terminal Connections: Cathode Band

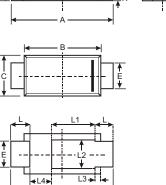
Terminals: Finish – Matte Tin annealed over Copper

leadframe. Solderable per MIL-STD-202,

Method 208 (e3)

Marking & Type Code Information: See Last Page

Ordering Information: See Last Page Weight: 0.01 grams (approximate)



PowerDI 123									
Dim	Min	Max	Тур						
Α	3.50	3.90	3.70						
В	2.60	3.00	2.80						
С	1.63	1.93	1.78						
D	0.93	1.00	0.98						
Е	0.85	1.25	1.00						
Н	0.15	0.25	0.20						
L	0.45	0.85	0.65						
L1	_	_	1.35						
L2	_	_	1.10						
L3	_	_	0.20						
L4	0.90	1.30	1.05						
All Dimensions in mm									

Maximum Ratings and Electrical Characteristics TA = 25 C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	DFLR1200	DFLR1400	DFLR1600	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	200	600	V		
RMS Reverse Voltage		V _{R(RMS)}	140	280	420	V	
Average Rectified Output Current (see fig	gure 4)	lo	1.0				
Non-Repetitive Peak Forward Surge Cur 8.3ms Single half sine-wave superimpos	I _{FSM}	25					
Forward Voltage	V _{FM}	1.1					
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			3.0 100			А	
Typical Total Capacitance (f = 1MHz, V _R = 4.0VDC)			10				

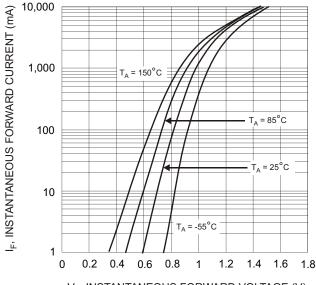
Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{JA}	134		°C/W
Thermal Resistance, Junction to Soldering Point (Note 3)	R JS		6	°C/W
Operating and Storage Temperature Range	T _{j,} T _{STG}		-65 to +150	С

es: 1. Device mounted on 1" x 1", FR-4 PCB; 2 oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf. TA = 25 C

- 2. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see EU Directive Annex Notes 5 and 7.
- 3. Theoretical R JS calculated from the top center of the die straight down to the PCB/cathode tab solder junction.





V_F, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 1 Typical Forward Characteristics

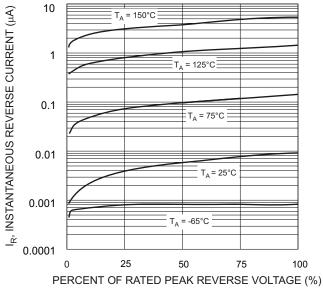
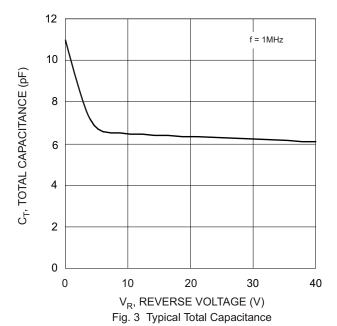
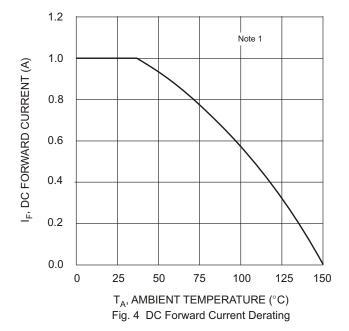


Fig. 2 Typical Reverse Characteristics







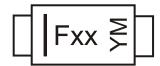
Ordering Information (Note 4)

Device	Marking Code	Packaging	Shipping		
DFLR1200-7	F12	PowerDI 123	3000/Tape & Reel		
DFLR1400-7	F14	PowerDI 123	3000/Tape & Reel		
DFLR1600-7	F18	PowerDI 123	3000/Tape & Reel		

Notes:

4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



Fxx = Product Type Marking Code (See Table Above)

YM = Date Code Marking Y = Year (ex: S = 2005)

M = Month (ex: 9 = September)

Date Code Key

Year	20	005	2006		2007	2008		2009	20	10	2011	2	2012
Code		S	Т		U	V		W		X	Υ		Z
Month		Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		1	2	3	4	5	6	7	8	9	0	N	D

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