

DFLU1200

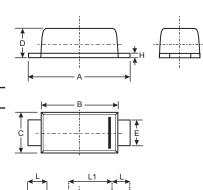
1.0A SURFACE MOUNT SUPER-FAST RECTIFIER PowerDI[™]123

Features

- Glass Passivated Die Construction
- Super-Fast Recovery Time for High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Note 2)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[™]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Marking & Type Code Information: See Last Page
- Ordering Information: See Last Page
- Weight: 0.01 grams (approximate)



L3-

4 4

	PowerDI [™] 123										
Dim	Min	Max	Тур								
Α	3.50	3.90	3.70								
В	2.60	3.00	2.80 1.78								
С	1.63	1.93									
D	0.93	1.00	0.98								
Е	0.85	1.25	1.00								
н	0.15	0.25	0.20								
L	0.45	0.85	0.65 1.35 1.10 0.20								
L1		_									
L2		_									
L3	_	_									
L4	0.90	1.30	1.05								
All	All Dimensions in mm										

Maximum Ratings and Electrical Characteristics TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage (Note 5)	V _{RRM} V _{RWM} V _R	200	V
RMS Reverse Voltage	V _{R(RMS)}	140	V
Average Rectified Output Current (see figure 4)	lo	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	IFSM	30	А
Forward Voltage Drop $@$ I _F = 0.6A $@$ I _F = 1.0A		0.90 0.98	V
Peak Reverse Current@ $T_A = 25^{\circ}C$ at Rated DC Blocking Voltage (Note 5)@ $T_A = 100^{\circ}C$		5.0 200	μA
Reverse Recovery Time (Note 4)	t _{rr}	25	ns
Typical Total Capacitance (f = 1MHz, V _R = 4VDC)	CT	27	pF

Thermal Characteristics

Characteristic	Symbol	Тур	Мах	Unit
Power Dissipation (Note 1) @ $T_A = 25^{\circ}C$	PD	—	1.0	W
Thermal Resistance Junction to Soldering Point (Note 3)	R _{0JS}	—	6	°C/W
Thermal Resistance Junction to Ambient (Note 1) @T _A = 25°C	R _{0JA}	116	_	°C/W
Thermal Resistance Junction to Ambient (Note 6) $@T_A = 25^{\circ}C$		182	_	°C/W
Operating and Storage Temperature Range		-65 to +150		

Notes: 1. Device mounted on 1" x 1", Polymide PCB; 2 oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf.

2. RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Notes 5 and 7.

3. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB cathode tab solder junction.

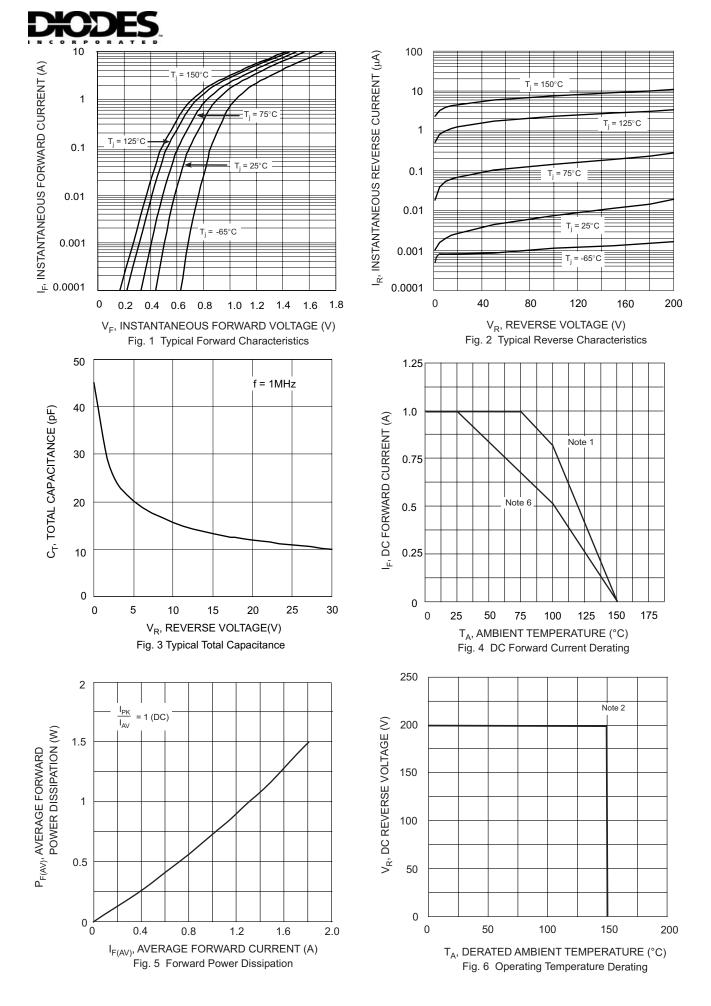
4. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A. See figure 7.

5. Short duration pulse test to minimize self-heating effect.

6. Device mounted on FR-4 PCB, 2 oz. Copper, minimum recommended pad layout pattern per http://www.diodes.com/datasheets/ap02001.pdf

DS30601 Rev. 3 - 2

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DS30601 Rev. 3 - 2 PowerDI is a trademark of Diodes Incorporated.



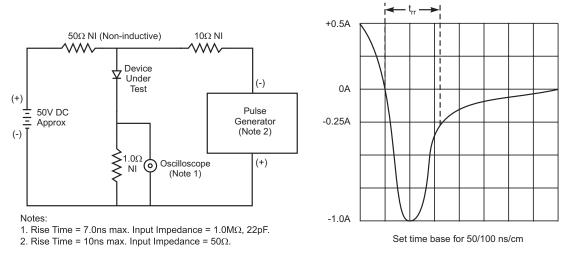


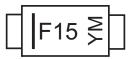
Fig. 7 Reverse Recovery Time Characteristic and Test Circuit

Ordering Information (Note 7)

Device	Marking Code	Packaging	Shipping		
DFLU1200-7	F15	PowerDI [™] 123	3000/Tape & Reel		

Notes: 7. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



F15 = Product Type Marking Code (See Table Above) YM = Date Code Marking Y = Year (ex: S = 2005) M = Month (ex: 9 = September)

Date Code Key

Year	200	5	20	006	20	007	2008		2009 W		2010	2011		2012 Z	
Code	S		٦	Т	I	U					Х	Y			
Month		Ja	n F	Feb	March	Apr	May	Jun	Jul	Aug	J Sep	Oct	Nov	Dec	
Code	•	1		2	3	4	5	6	7	8	9	0	Ν	D	

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